



# Transformers

## Transformers for IGBT/FET

**Series/Type:** EP9 series  
**Ordering Code:** B82804E  
**Date:** August 2024

### Construction

- Ferrite core MnZn
- SMD L pins

### Features

- Height: 11 mm max
- Footprint: 13 mm x 11 mm
- Wide temperature range from  $-40^{\circ}\text{C}$  up to  $+150^{\circ}\text{C}$
- Qualified to AEC-Q200 REV E
- RoHS compatible
- Very low coupling capacity in the range of 2 pF
- Suitable for lead-free reflow soldering as referenced in JEDEC-J-STD 020F



### Applications

- DC/DC power supplies
- Push-pull converters
- Isolated DC/DC Converters
- Gate drive transformers for IGBT/MOSFET

### Insulation Characteristics

- N1/ N2, N3: Creepage  $\geq 5$  mm, Clearance  $\geq 5$  mm [cumulative, core floating]
- Bobbin plastic material – CTI IIIa

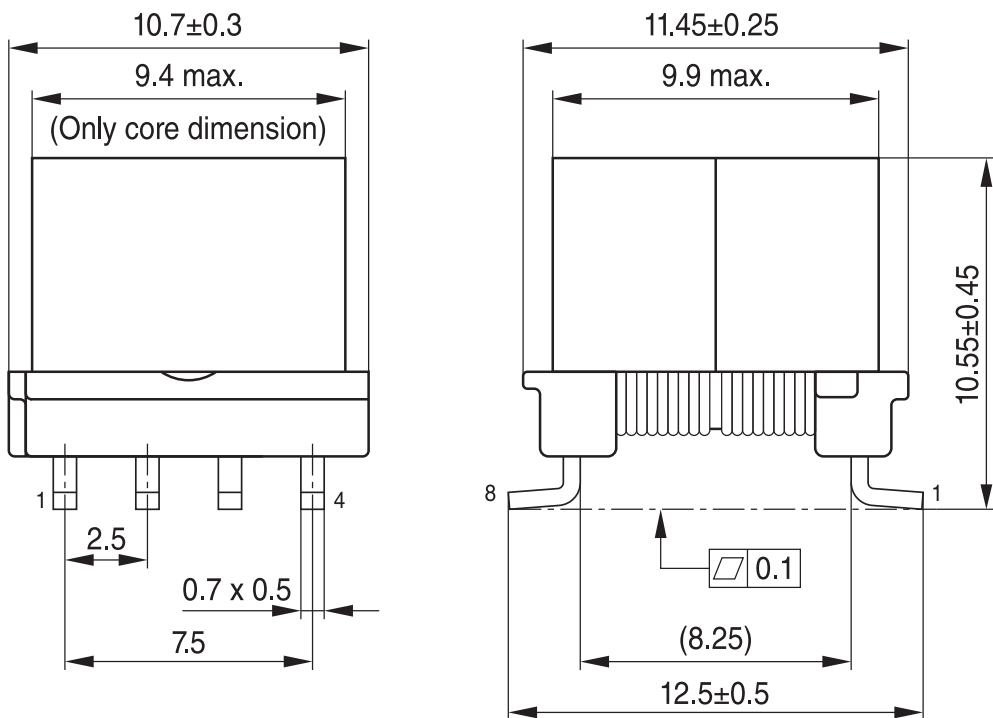
### Marking

- Product brand, middle block of ordering code, date code, pin 1 marker, production place identification code

### Delivery mode

- Tape and reel 330 mm diameter
- Packing unit 250 pcs per reel

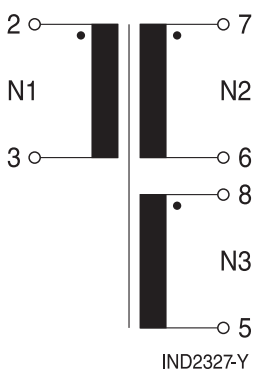
**Dimensional Drawing and Layout Recommendation**



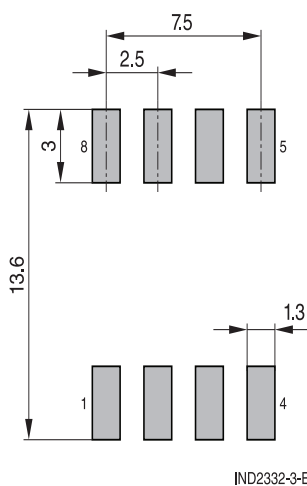
IND2325-W-E

Dimensions in mm

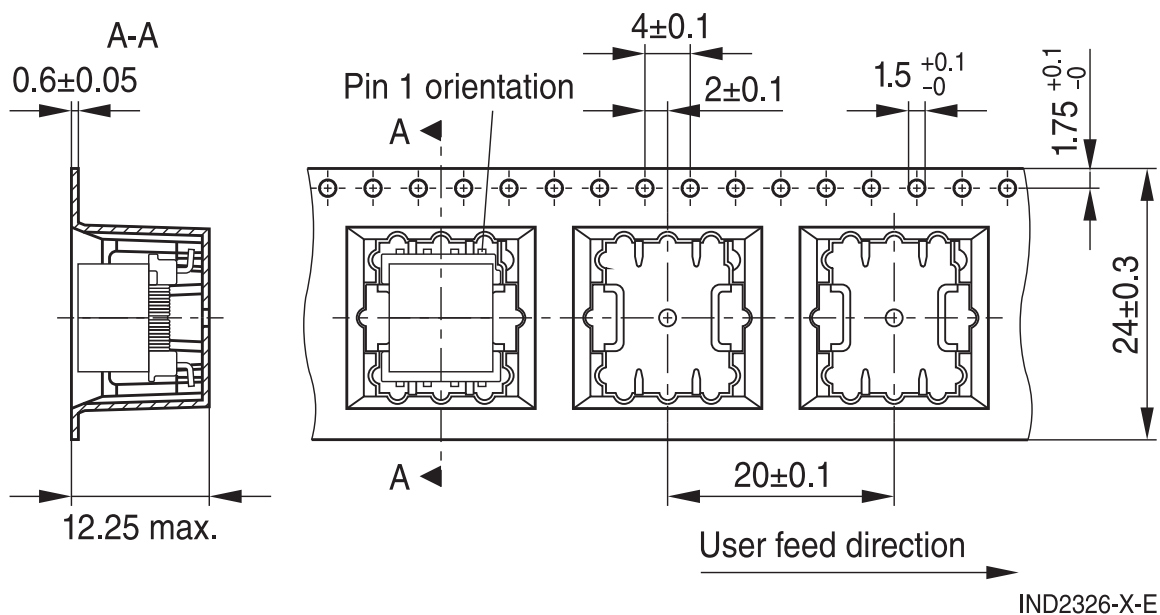
**Schematics**



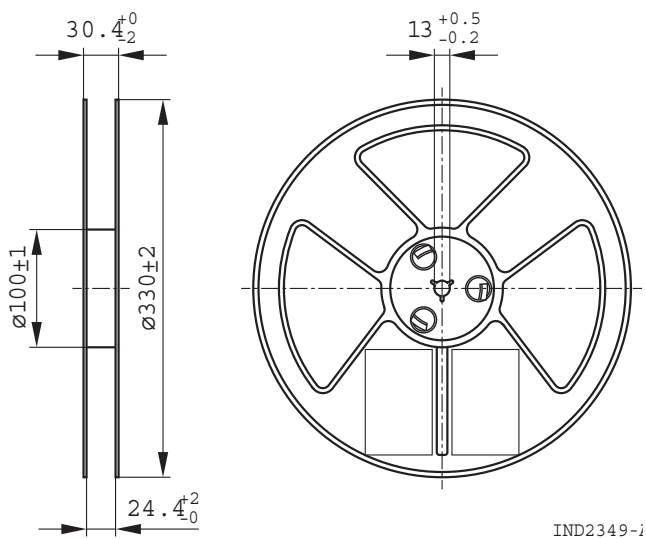
**Recommended PCB layout (Top view)**



**Blister Tape**



**Reel**



**Technical data and measuring conditions**

specified @ 25°C if not mentioned otherwise, all values without tolerance are typical values

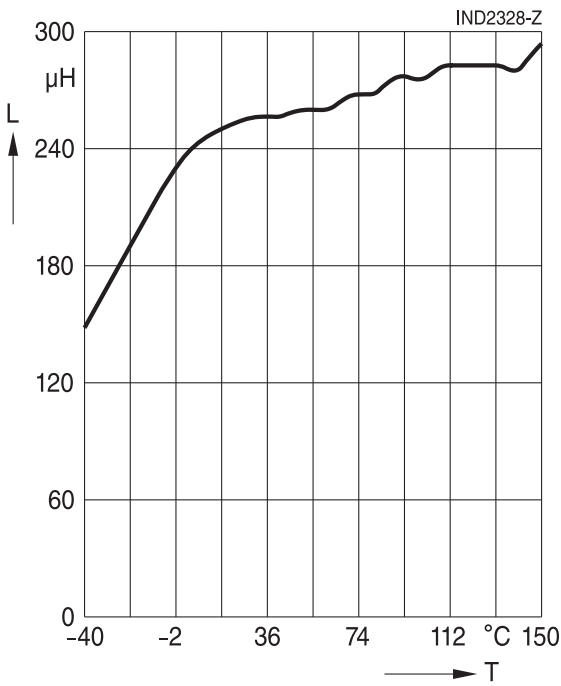
Typical operational frequency	100 kHz ... 400 kHz
Rated Inductance $L_{N1,typ}$	Measured @ 100 kHz, 100 mV
Leakage inductance $L_{leak, N1}$	Measured @ 100 kHz, 100 mV
Parasitic capacitance $C_p$ N1 / N2, N3	Measured @ 100 kHz, 1 V: 2 pF
High Voltage Test N1 / N2, N3 Routine test	2500 V AC, 50 Hz, 1 s
High Voltage Test N1 / N2, N3 Type test	2500 V AC, 50 Hz, 1 min
Voltage time product ( $E \cdot dt_{N1, max}$ )	$T \leq +150$ °C
Storage conditions	-25 °C ... +40 °C, humidity $\leq$ 75% RH
Resistance to reflow heat	In accordance with JEDEC J-STD-020F +245 °C ( $T_{Peak}$ -5 °C for 30 seconds)
Solderability (lead-free)	Sn96.5Ag3.0Cu0.5: +(245 $\pm$ 3) °C, (3 $\pm$ 0.3) s Wetting of soldering area $\geq$ 95% (to IEC 60068-2-58, test Td1, method 1)
Operating temperature range	-40°C ... 150°C (component)
Weight	Approx. 2.5 g

**Characteristics and ordering codes**

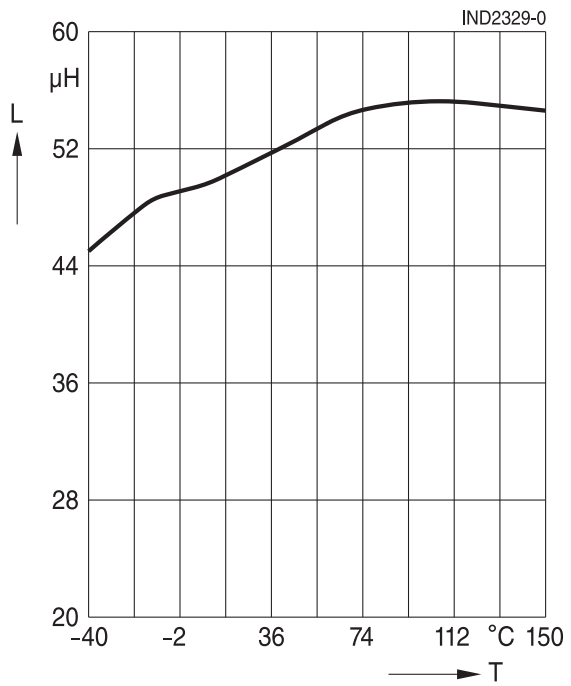
Topologies	Turns ratio N1 / N2, N3	$L_{N1}$ typ  $\mu$ H	$L_{leak, typ, N1}$  $\mu$ H	$E \cdot dt_{N1, max}$ (unipolar/ bipolar)  $\mu$ Vs	$R_{DC, N1}$  m $\Omega$	$R_{DC, N2}$  m $\Omega$	$R_{DC, N3}$  m $\Omega$	Ordering code
Half bridge	1 : 2.8 : 1.53	260	5	20 / 40	180	1250	780	B82804E0164A200
Push Pull	1 : 2.9 : 1	47	1.7	15 / 30	140	400	160	B82804E0473A200

Typical Inductance  $L_{N1}$  vs Temperature graph

B82804E0164A200

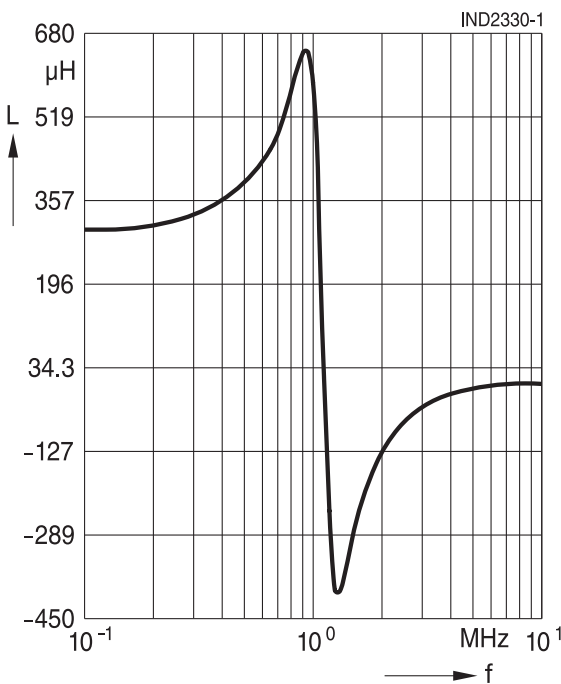


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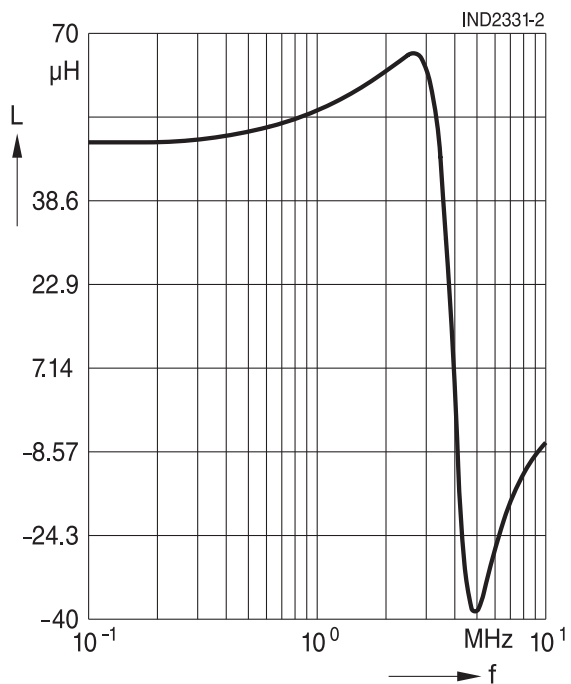


Typical Inductance  $L_{N1}$  vs Frequency graph

B82804E0164A200



B82804E0473A200



## Cautions and warnings

- Please note the recommendations in our Inductors data book (latest edition), online catalogs and in the data sheets.
  - Particular attention should be paid to the derating curves, if given. Derating applies in the case the ambient temperature in application exceeds the rated temperature of the component.
  - Ensure the operation temperature of the component in application not to exceed the maximum specified value or the upper climatic category temperature.
  - The soldering conditions should also be observed. Temperatures quoted in relation to wave soldering refer to the pins only. Temperatures specified in relation to reflow soldering can also refer to the pins or terminals for products with larger thermal mass, as in such cases, the temperature difference to the top of the component is too big (e.g., high proportion of core within the component).
- If the components are to be washed varnished it is necessary to check whether the washing varnish agent that is used has a negative effect on the wire insulation, any plastics that are used, or on glued joints. It is possible for washing varnish agent residues to have a negative effect in the long-term on wire insulation.
 

Washing processes may damage the product due to the possible static or cyclic mechanical loads (e.g., ultrasonic cleaning). They may cause cracks to develop on the product and its parts, which might lead to reduced reliability or lifetime.
- The following points must be observed if the components are potted, sealed, or varnished in customer applications:
  - Many potting, sealing, or varnishing materials shrink as they harden. They therefore exert a pressure on the plastic housing or core. This pressure can have a deleterious effect on electrical properties, and in extreme cases can damage the core or plastic housing mechanically.
  - It is necessary to check whether the potting, sealing or varnishing materials used attack or destroy the wire insulation, plastics, or glue.
  - The effect of the potting, sealing, or varnishing materials may change the high-frequency behavior of the components.
- Magnetic core materials such as ferrites are sensitive to direct impact. This can cause the core material to flake or lead to breakage of the magnetic core material.
- Any type of tension or pressure on the product may result in damage and affect its functionality and reliability.
  - The products are only to be attached to fixings or mounting holes provided for this purpose in accordance with the data sheet.
  - If additional mechanical forces are applied to the component, e.g., application of gap pads, it is necessary to check whether they attack or destroy any part of the component.
  - It is not permitted for the product specified in the data sheet to assume a mechanical function in the final application.
- Inductance value can drop if external metallic or magnetic parts will be put close to the coil or into the air gap of the coil or core or magnetic material.
- Even for customer-specific products, conclusive validation of the component in the circuit can only be carried out by the customer.

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## Important Notes

- 8 The trade names EPCOS, CarXield, CeraCharge, CeraDiode, CeraLink, CeraPad, CeraPlas, CSMP, CTVS, DeltaCap, DigiSiMic, FilterCap, FormFit, InsuGate, LeaXield, MediPlas, MiniBlue, MiniCell, MKD, MKK, ModCap, MotorCap, PCC, PhaseCap, PhaseCube, PhaseMod, PhiCap, PiezoBrush, PlasmaBrush, PowerHap, PQSine, PQvar, SIFERRIT, SIFI, SIKOREL, SilverCap, SIMDAD, SiMic, SIMID, SineFormer, SIOV, SurfIND, ThermoFuse, WindCap, XieldCap are **trademarks registered or pending** in Europe and in other countries. Further information will be found on the Internet at [www.tdk-electronics.tdk.com/trademarks](http://www.tdk-electronics.tdk.com/trademarks).

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